



DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA
SURATHKAL, SRINIVASNAGAR 575025 KARNATAKA, INDIA

24th January 2022

Corrigendum-3 to Advertisement Date on 6th December 2021 for the post of JRF

The **eligibility criteria are changed** for this published advertisement. Last date for submission of application for the post of JRF to work on the R&D project titled “**Development of Highly Conductive Ultrathin VS₂ Crystals for High-Performance Flexible Supercapacitors**” supported by Science and Engineering Research Board (SERB), DST, Govt. of India has been extended **up to 4th February 2022**.

Sushil Kumar Pandey

Dr. Sushil Kumar Pandey

Project Investigator and Assistant Professor,
Department of E & C Engg.
NITK, Surathkal, Mangaluru, Karnataka, India



Department of Electronics and Communication Engineering
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL
Under Ministry of Education, Government of India

Advt. No.: 02/NITK/2021

Dated: 06/12/2021

Advertisement for the Position of JRF

Applications are invited from the interested candidates for the post of JRF to work on the R&D project titled “**Development of Highly Conductive Ultrathin VS₂ Crystals for High-Performance Flexible Supercapacitors**” supported by Science and Engineering Research Board (SERB), DST, New Delhi. The selected candidate will be appointed for one year (on contract).

S No.	Position	Area of Specialization	Maximum Duration	Consolidated Salary	Number of Positions
1	JRF- Junior Research Fellow	Growth and Characterization of Electronic Materials and Devices	12 months	Rs. 31,000/- (Per month for one years) plus HRA as per norms	One (01)

Eligibility:

Modified Educational Qualification:

B.Tech/B.E./M.Tech./M.E./MS(R)/equivalent in Electronics and Communication Engineering/Electronics Engineering/Electrical Engineering/Material Science & Engineering/Metallurgical and Materials Engineering/Mechanical Engineering/Chemical Engineering/Nanotechnology with desirable knowledge/expertise in growth and characterization of electronic materials and devices. Those who have M.Sc. degree in Electronics are also eligible. The GATE/NET qualification is necessary for undergraduates and desirable for postgraduates. Candidates must have at least 6.5/10 CGPA or 60 percent marks in aggregate from a recognized technical institute or university as a full time program. Proof of B.Tech/B.E./M.Tech./M.E./MS(R)/equivalent certificate has to be provided during the time of interview.

These are the minimum eligibility criteria. The short-listing criteria may vary according to the number of application received.

Age limit: Not more than 32 Years. Age relaxation will be provided as per SERB-DST, GoI norms.



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Application Process:

Interested candidates must apply with the following documents: (1) Cover letter (2) Bio-data with passport-sized photograph, (3) Scanned copies of educational certificates and mark sheets, class XII onwards (4) GATE qualified certificate and (5) Scanned copies of Proof for research experience, special achievements and publications, if any. **The soft copies of all the above documents (pdf format) must be emailed to the Principal Investigator (Dr. Sushil Kumar Pandey, Email id: skpandey@nitk.edu.in) on or before 27th December 2021 and hard copies of all the documents should be send to the address for correspondence is given below.** No TA/DA will be paid for attending the written test/interview. The appointment will be on a purely temporary basis co-terminus with the project. Candidates who are already employed should produce relieving certificate from their employers, if selected. **The shortlisted candidates with the date of written test/interview will be intimated by email only.**

Address for Correspondence:

To,
Dr. Sushil Kumar Pandey
Principal Investigator and Assistant Professor
E&C Engineering Department
NITK Surathkal
Mangalore-575025
India.

Mobile: +917052502636
Email: skpandey@nitk.edu.in

NOTE: The envelope containing the application should be super scribed as “Application for the position of JRF under SERB project on “Development of Highly Conductive Ultrathin VS₂ Crystals for High-Performance Flexible Supercapacitors”.



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Application for the Position of Junior Research Fellow under SERB

1. Post Applied for: JRF-Junior Research Fellow

2. Name of the Candidate (BLOCKLETTER):

3. Father's Name (BLOCKLETTER):

4. Mother's Name (BLOCKLETTER):

5. (a) Date of Birth: (DD/MM/YYYY)_____

(b) Sex (Male/Female/Other):__ _

(c) Marital Status: Married/Single

(d) Category: SC/ST/OBC/PWD/Open

6. Previous Research experience: (use additional sheet if required)_____

7. Publication(s), if any: (use additional sheet if required)_____

8. GATE/ NET (if any): Qualified (Yes/No): Score: _____ Rank: _____
Specialization: _____ Year: _____

9. Academic Qualification: (Starting from Standard 10 or equivalent Examination)

Name of Exam Passed	Name of the School/College/Institute/ University	Year of Passing	Discipline/ Specialization	Percentage of Marks/ CGPA

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10. (a) Address for Communication: (BLOCKLETTER):

(b) Contact No (Mob):

(c) E-mail ID :

11. Contact Details of two referees:

	Referee I	Referee II
Name :		
Designation :		
Organization:		
Office Address :		
Office Phone Number:		
Email ID:		

12. Experience if any (in years and use additional sheet if required)

I do here by declare that the information furnished in this application is true to the best of my knowledge and belief. If selected, I promise to abide by the rules and regulations of the Institute.

Date:

Place:

Signature

Note- If any information is required, extra A4 sheet may be attached.